IN THE CLAIMS:

Amend claims 1-10 and cancel claim 11 as follows:

1. (Currently Amended) A parametric recursive digital filter having a cut-off/center frequency, the ~~said~~ digital filter comprising:

a delay unit having at least two delay elements and an interconnected phase network that includes ~~an~~ controllable phase angle, where the cut-off/center frequency of the ~~said~~ digital filter is set as a function of the ~~said~~ controllable phase angle;

a positive feedback network connected to a first one of the ~~said~~ delay elements~~unit~~ creating a positive feedback path; and

a feedback network connected to ~~said~~ a second one of the delay elements~~unit~~ creating a feedback path connected to the output of the second delay element in the delay unit.

2. (Currently Amended) The filter of ~~according to~~ claim 1, where ~~in which~~ a plurality of delay units are provided.

3. (Currently Amended) The filter of ~~according to~~ claim 2, where ~~in which~~ the delay units are identically designed and are controlled in the same manner.

4. (Currently Amended) The filter of ~~according to~~ claim 2~~1~~, where ~~wherein the~~ each delay unit comprises at least two ~~-~~delay elements.

5. (Currently Amended) The filter of ~~according to~~ claim 2, ~~1,~~ where ~~wherein~~ the positive feedback network comprises a plurality of positive feedback paths.

6. (Currently Amended) The filter according to claim 2, ~~1, wherein~~ where the feedback network comprises a plurality of feedback paths.

7. (Currently Amended) The filter of ~~according to~~ claim 1, where the ~~wherein said~~ delay unit comprises an all-pass filter.

8. (Currently Amended) The filter of ~~according to~~ claim 7, where the ~~wherein said~~ all-pass filter comprises:

a first adder, one input of which forms an~~the~~ input of the all-pass filter~~delay unit~~,

a second adder, the output of which forms an~~the~~ output of the all-pass filter~~delay unit~~,

a coefficient section which is connected between the output of the first adder and a first input of the second adder,

a first delay element which is connected between the input of the all-pass filter~~delay unit~~ and a second input of the second adder,

a second delay element which is connected between the output of the second adder~~delay unit~~ and a second input of the first adder,

the phase angle of the filter element being adjustable by changing the coefficient of the coefficient section, and

the output of the first and/or second delay element being provided for connecting a feedback path.

9. (Currently Amended) The filter of ~~according to~~ claim 29, where ~~in which~~ two delay units comprising delay elements are interconnected with one another in such a manner that only a total of three delay elements are provided, one delay element being attributable to both delay units.

10. (Currently Amended) The filter of ~~according to~~ claim 6, where ~~in which~~ a frequency-influencing filter unit is provided as delay unit.

11. (Cancelled)